Job Description					
Job Title	Engg-HW design D&D	– Embedded Systems –	Job Code	DND080	
Department	D&D		L	1	
Location	Noida				
Job Summary	Responsible for schematic design, PCB verification, prototyping, board testing, production training etc. Will be responsible for parametric testing, certification testing from external labs, test procedure creation and test zig creation. Will also assist validation team in preparing negative/positive test cases to be executed during product validation.				
Working Relationships	Reporting to: Team Lead-Hardware Responsible for managing: Technician Other Key Relationships: a) Interacting with SW Team for matters related to prototyping, initial board testing, test zig creation, hardware driver integration, ckt training, module testing and documentation related to board preparation. b) Interacting with HW-QA team for matters related to SW-QA compliances. c) Interacting with production team for proto-typing, imparting board level testing training, debugging training d) Interacting with purchase team for handing over purchase related information e) Interacting with validation team for initial board preparation training, assisting on positive and negative test case preparation. f) Interacting with technical documentation team for providing input for technical documentation g) Interacting with ESG team for proper training and handover of ESG team all				
	hardware boards to be transferred to factory h) Interacting with external LAB for certification testing				
Functions & Responsibilities	a) Low leve b) Schemat c) Prototyp d) Designing e) Following modules f) Preparat g) Device di h) Creation i) Preparat test repo j) Imparting card mar k) Conducti skill set l) Managin	I HW design document pro- ic designing, netlist creating ing of PCB boards g of various harnesses, exi- g the rule set prepared for ion and execution of modificer integration with SW in of simulation for interface ion of all production hand out formats, training documents integration in the factory	eparation. Ing, gerber verification, gerber verification, gerber verification, gerber verification, gerber verification, gerber verification, with the ents, wiring direct of components of component	commenting of the HW nd report preparation gs etc. s like test procedures, agrams etc r smoother handover of Support for refreshing the	

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	initial HW training.	
	n) Carrying out parametric testing in internal labs and external labs	
	o) Carrying out various hardware certifications from external lab	
	p) Assisting technical document team in preparing the technical manuals,	
	trouble shooting guides, repair manual by providing relevant inputs	
	a) B.E./B. Tech/Diploma (Electrical/Electronics)	
	 b) 2+ years of experience with requisite training in hardware designing tools and equipments 	
	c) For diploma holder 3+ years of experience is required	
	d) Hands on experience of Schematic Design Tools and PCB design tools	
	e) Proficiency in schematic design, netlist generation, BoM creating – Trained	
	from training institute or prior experience	
	f) Proficiency in power supplies/processor/micro-controller, interfaces and	
Requirements	control.	
	 g) Proficiency of various system buses, communication protocols, ethernet protocols and tools. 	
	h) Complete understanding of hardware design life cycle.	
	i) Experience in proto-typing and testing	
	j) Understanding of software driver integration	
	k) Basic knowledge of general protocols, system buses and interfaces	
	l) Basic knowledge of higher level documentation tools such as PPT, Visio,	
	Snagit etc	
	Recruitment for this position shall be carried out preferably through	
Job posting	Hardware/Embedded system training institutes.	
requirements	Face-to-face interview shall be conducted for scrutinizing the resource.	
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